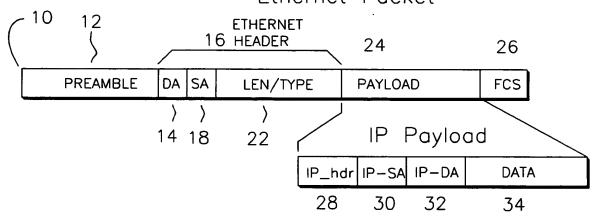
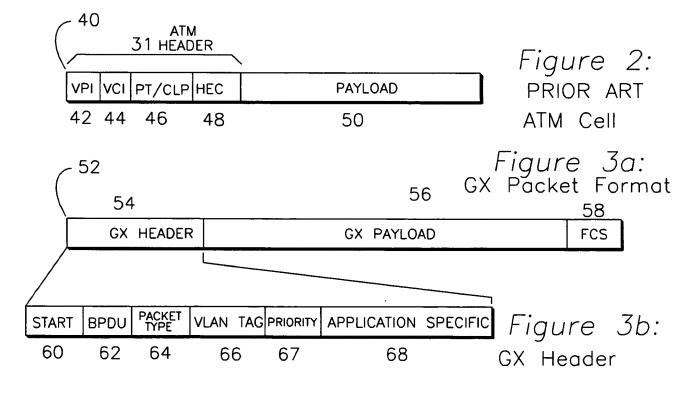
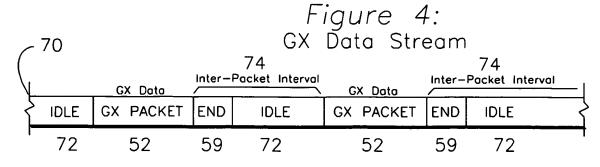


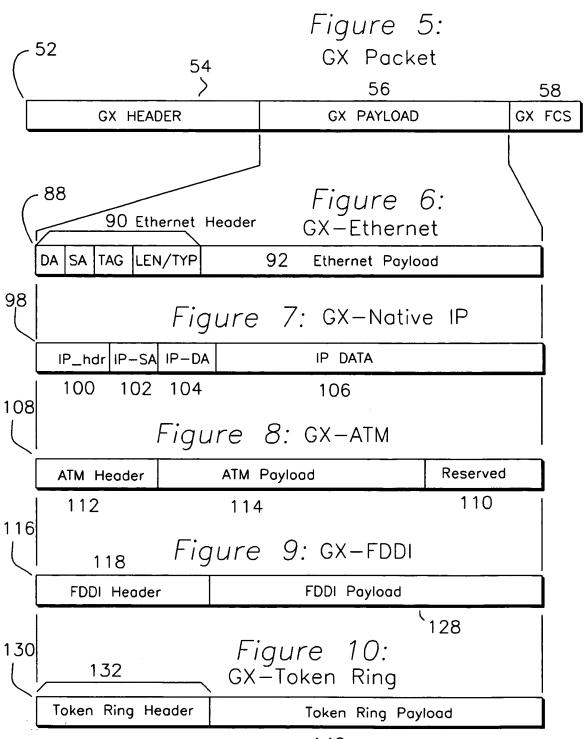
Figure 1:
PRIOR ART
Ethernet Packet











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<i>چا</i>											
	1	50)			1	52			_	Figure 11:
154-			0	1	2	3	4	5	6	7	GX Packet Format
	156	e`	K	K	K	K	K	Κ	K	K	167 (n=8)
	158	0	R	R	R	R	R	R	R	R	Inter-Packet
	160	е	Κ	Κ	K	K	K	Κ	K	K	ldle
	162	0	R	R	R	R	R	R	R	R	
	164	е	h0	h1	h2	h3	h4	h5	h6	h7	GX Header
	166	0	d0	d1	d2	d3	d4	d5	d6	d7	
		е	d0	d1	d2	d3	d4	d5	d6	d7	
		0	d0	d1	d2	d3	d4	d5	d6	d7	- I
		е	d0	d1	d2	d3	d4	d5	d6	d7	168 64 byte
		0	d0	d1	d2	d3	d4	d5	d6	d7	GX Payload 64 byte Data Packet
		е	d0	d1	d2	d3	d4	d5	d6	d7	including FCS
		0	d0	d1	d2	d3	d4	d5	d6	d7	
	170	е	d0	d1	d2	d3	FO	F1	F2	F3	
	171	0	Т	R	R	R	R	R	R	R	Inter-Packet Idle
	172	е	h0	h1	h2	h3	h4	h5	h6	h7	GX Header
	173	0	d0	d1	d2	d3	d4	d5	d6	d7	
		е	90	d1	d2	d3	d4	d5	d6	d7	
		0	d0	d1	d2	d3	d4	d5	d6	d7	
		е	d0	d1	d2	d3	d4	d5	d6	d7	
		0	d0	d1	d2	d3	d4	d5	d6	d7	4 (
							\searrow				Ethernet Packet
		0	d0	d1	d2	d3	d4	d5	d6	d7	
	174	е	d0	d1	d2	d3	d4	d5	d6	d7	<u> </u>
	175	0	d0	d1	d2	F0	F1	F2	F3	Τ	
	176	е									- 1
101%	177	0	RB	RB	RB	RB	RB	RB	RB	RB	Inter-Packet Busy Idle 181
	178	е	KB	KB	ΚB	ΚB	ΚB	ΚB	KB	KB	
	1.79	Ò	RB	RB	RB	RB	RB	RB	RB	RB	1 /



Figure 12:

GX Packet Format (n=4)

Figure 13:

GX Packet Format (n=2)

		ζ.		,					,	(11-	۷)		
190 192							•	272	2 2 7	78			
194	<u></u>	1	2	3			230 -			0	1		
196 e	К	Κ	K	K		224		232	ė	Κ	Κ		274
198 o	R	R	R	R		Inter—pack dle	et	234	0	R	R		Inter-packet idle
200 e	K	Κ	K	K	'	Oie		236	е	Κ	Κ		1016
202 o	R	R	R	R	<u> </u>	2	239	238	0	R	R		
204 e	h0	h1	h2	h3		226		240	е	h0	h1		GX Header
206 o	h4	h5	h6	h7	JGX	Header		242	0	h2	h3		
208 e	d0	d1	d2	d3				244	е	h4	h5		
210 _o	d4	d5	d6	d7				246	0	h6	h7		
212 e	d0	d1	d2	d3		228		248	е	d0	d1		276
214 o	d4	d5	d6	d7	GX	Payload		250	0	d2	d3		64 byte Data Packet
е	d0	d1	d2	d3	6	4 byte		252	е	d4	d5		including FCS
0	d4	d5	d6	d7		ta Packet ncluding		254	0	d6	d7		
е	d0	d1	d2	d3	"	FCS	}		e –		\searrow	-	
0	d4	d5	d6	d7					0	d0	ď1		
е	d0	d1	d2	d3					е	d2	d3		
0	d4	d5	d6	d7				256	0	d4	d5		
е	d0	d1	d2	d3				258	е	d6	d7	•	
0	d4	d5	d6	d7		•		260	0	FΟ	F1		
е	d0	d1	d2	d3				262	е	F2	F3		
0	d4	d5	d6	d7				264	0	Т	R		
е	d0	d1	d2	d3				266	е	κ(K		
216 o	ĘΟ	F1	F2	F3		/	J		٦		\bigvee	_	
218 e	T	ΚB	ΚB	ΚB					е	Κ	K		
220 o	RB	RB	RB	RB					0	R	R		
222 e	Κ	K	K	K					е	K	K		



Figure 14:
GX Packet Format (n=1)

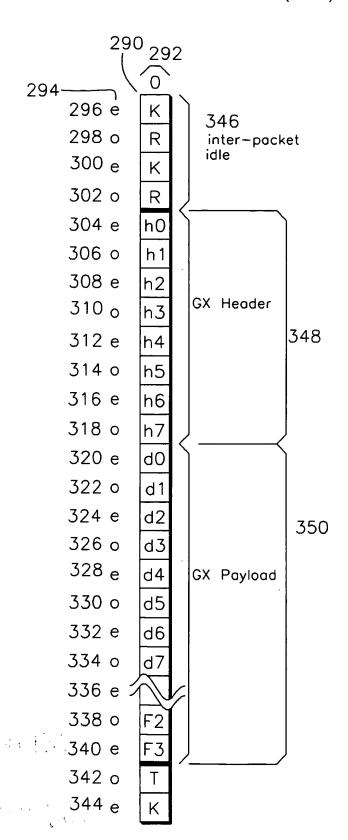




Figure 15: Transmit Processor (n=8)

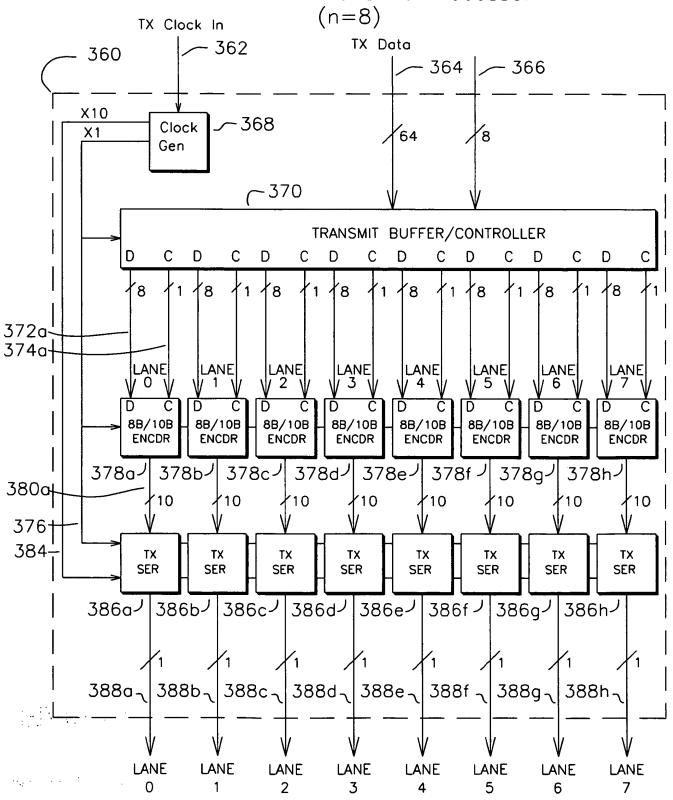




Figure 16: Transmit Processor (n=4)

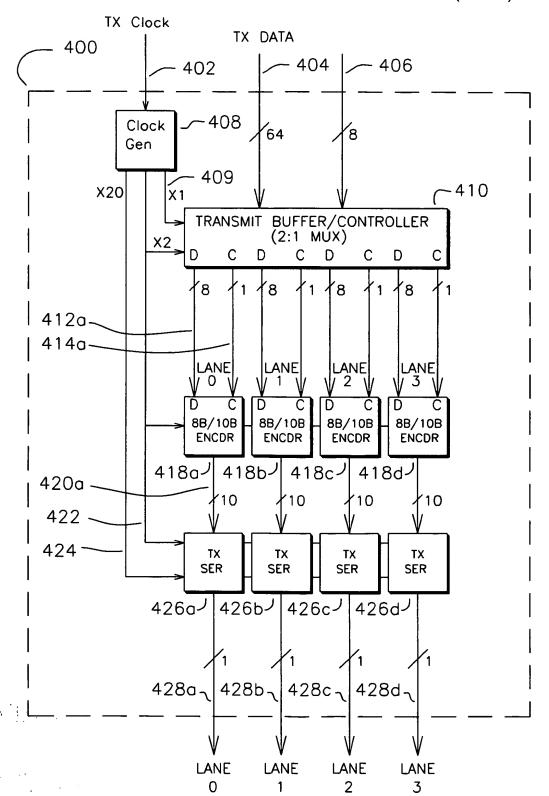




Figure 17a: 8B/10B Encoder

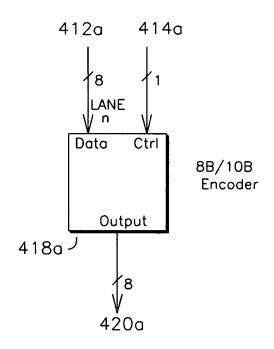


Figure 17b: 8B/10B Encoder

	8B Input (Ctrl Input	10B Output
440	START	CTRL	Start
442	8B_Data	DATA	10B_Data
444	END	CTRL	End
446	IDLE-EVEN	CTRL	Even_ldle
448	IDLE-ODD	CTRL	Odd_Idle
449	IDLE-EVEN_BUSY	CTRL	Even_Idle_Busy
450	IDLE-ODD_BUSY	CTRL	Odd_Idle_Busy



Figure 18:
Receive Processor (n=8)

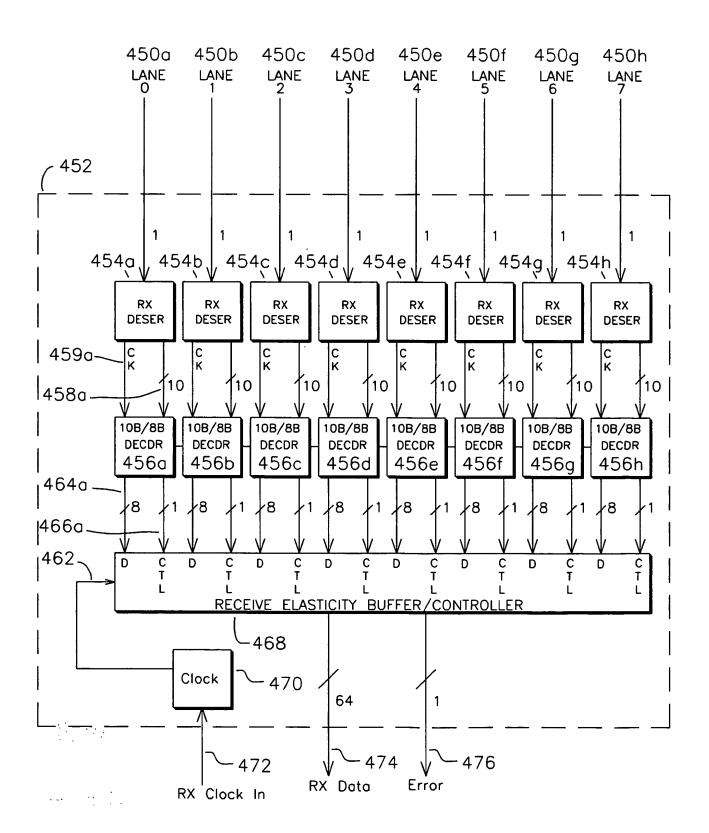




Figure 19: Receive Processor (n=4)

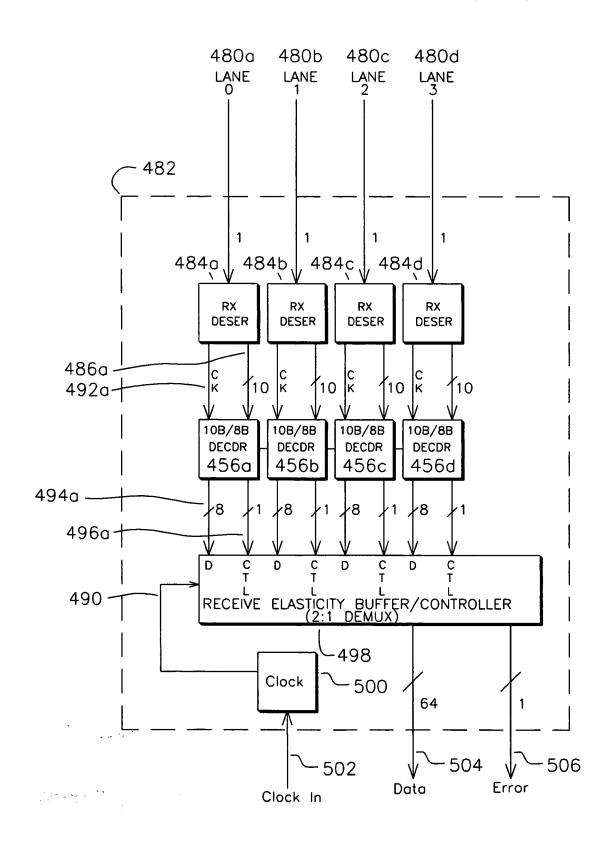




Figure 20a: 10B/8B Decoder

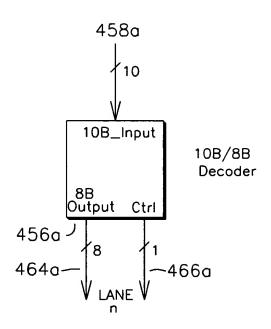


Figure 20b: 10B/8B Decoder

	10B Input	8B Output	Ctrl Out	put
470	Start	START	CTRL	
472	10B_Data	8B_Data	DATA	
474	End	END	CTRL	
476	Even_Idle	IDLE-EVEN	CTRL	
478	Odd_ldle	IDLE-ODD	CTRL	
480	Even_Idle_Busy	IDLE-EVEN-BUSY	CTRL	
482	Odd_ldle_Busy	IDLE-ODD-BUSY	CTRL	